

**Features:**

- n Isolated mounting base 3000V~
- n Pressure contact technology with increased power cycling capability
- n Space and weight saving

**Typical Applications**

- n AC/DC Motor drives
- n Various rectifiers
- n DC supply for PWM inverter

V <sub>RRM</sub> , V <sub>DRM</sub>	Type & Outline	
<b>800V</b>	MTx570-08-416F3	MFx570-08-416F3
<b>1000V</b>	MTx570-10-416F3	MFx570-10-416F3
<b>1200V</b>	MTx570-12-416F3	MFx570-12-416F3
<b>1400V</b>	MTx570-14-416F3	MFx570-14-416F3
<b>1600V</b>	MTx570-16-416F3	MFx570-16-416F3
<b>1800V</b>	MTx570-18-416F3	MFx570-18-416F3
<b>1800V</b>	MT570-18-416F3G	

MTx stands for any type of **MTC, MTA, MTK**  
 MFx stands for any type of **MFC, MFA, MFK**

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T <sub>j</sub> (°C)	VALUE			UNIT
				Min	Type	Max	
I <sub>T(AV)</sub>	Mean on-state current	180° half sine wave 50Hz	125			570	A
I <sub>T(RMS)</sub>	RMS on-state current	Single side cooled, T <sub>c</sub> =85°C				895	A
I <sub>DRM</sub> I <sub>RRM</sub>	Repetitive peak current	at V <sub>DRM</sub> at V <sub>RRM</sub>	125			35	mA
I <sub>TSM</sub>	Surge on-state current	V <sub>R</sub> =60%V <sub>RRM</sub> , t=10ms half sine.	125			15	kA
I <sup>2</sup> t	I <sup>2</sup> t for fusing coordination		125			1125	10 <sup>3</sup> A <sup>2</sup> s
V <sub>TO</sub>	Threshold voltage		125			0.80	V
r <sub>T</sub>	On-state slope resistance					0.20	mΩ
V <sub>TM</sub>	Peak on-state voltage	I <sub>TM</sub> =1710A	25			1.45	V
dv/dt	Critical rate of rise of off-state voltage	V <sub>DM</sub> =67%V <sub>DRM</sub>	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t <sub>r</sub> ≤ 0.5μs Repetitive	125			200	A/μs
I <sub>GT</sub>	Gate trigger current	V <sub>A</sub> =12V, I <sub>A</sub> =1A	25	30		200	mA
V <sub>GT</sub>	Gate trigger voltage			1.0		3.0	V
I <sub>H</sub>	Holding current			10		200	mA
I <sub>L</sub>	Latching current					1500	mA
V <sub>GD</sub>	Non-trigger gate voltage	V <sub>DM</sub> =67%V <sub>DRM</sub>	125			0.20	V
R <sub>th(j-c)</sub>	Thermal resistance Junction to case	Single side cooled per chip				0.065	°C/W
R <sub>th(c-h)</sub>	Thermal resistance case to heatsink	Single side cooled per chip				0.024	°C/W
V <sub>iso</sub>	Isolation voltage	50Hz, R.M.S, t=1min, I <sub>iso</sub> : 1mA(MAX)		3000			V
F <sub>m</sub>	Terminal connection torque(M10)			10.0		12.0	N·m
	Mounting torque(M6)			4.5		6.0	N·m
T <sub>vj</sub>	Junction temperature			-40		125	°C
T <sub>stg</sub>	Stored temperature			-40		125	°C
W <sub>t</sub>	Weight				1540		g
Outline	416F3						

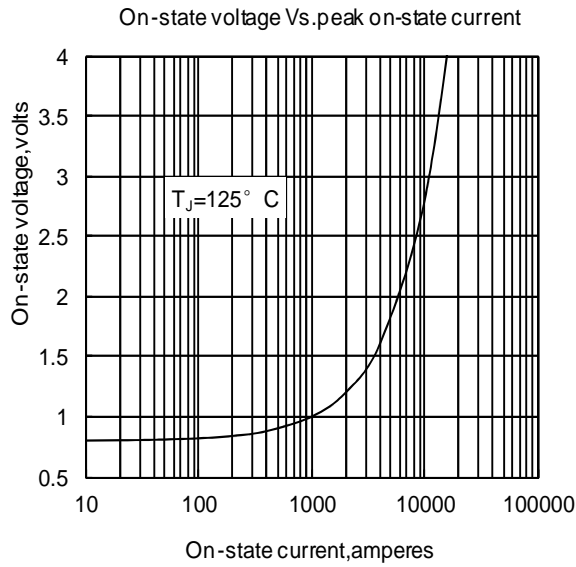


Fig1

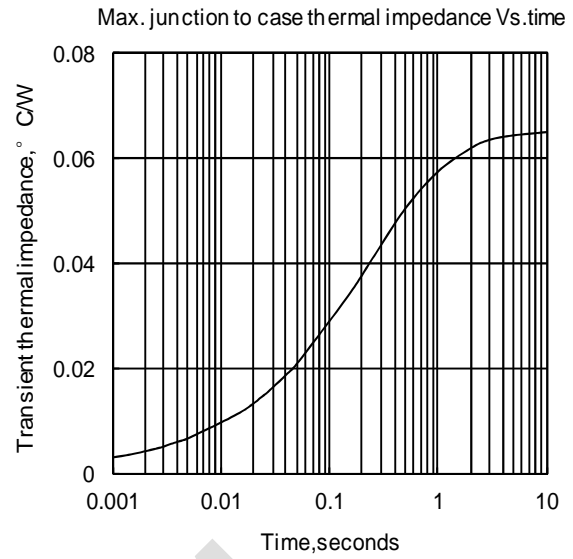


Fig2

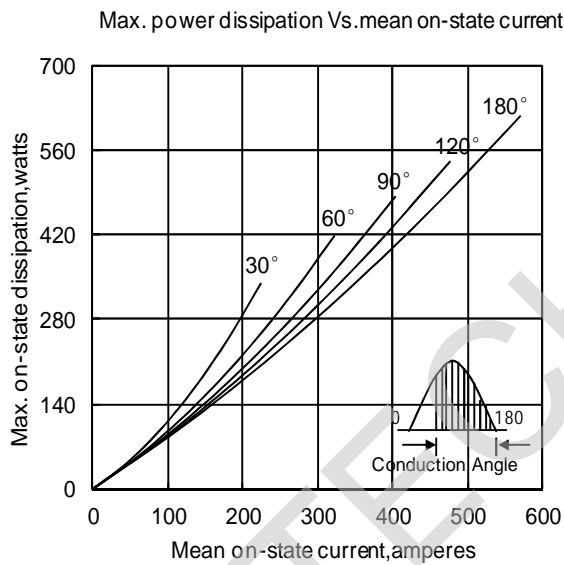


Fig3

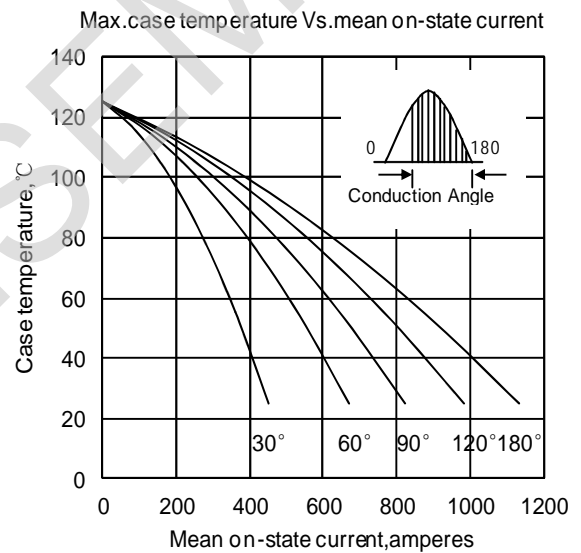


Fig4

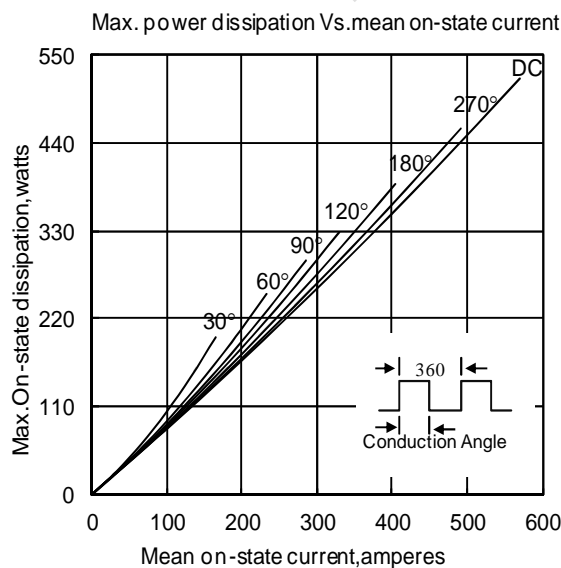


Fig5

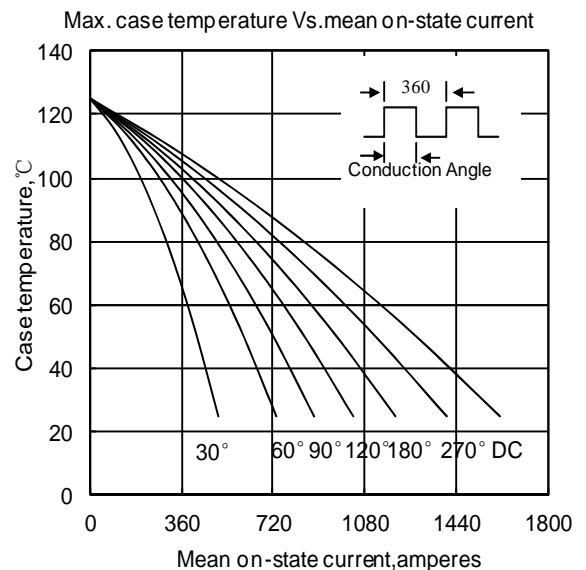


Fig6

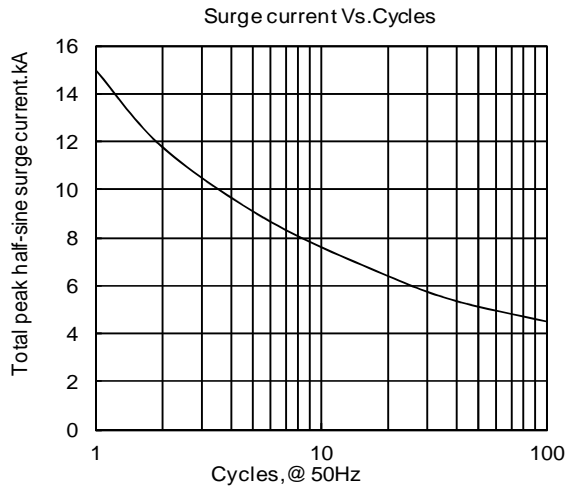


Fig 7

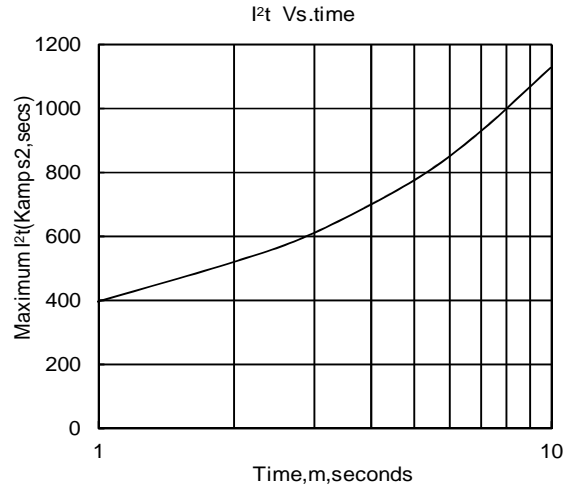


Fig 8

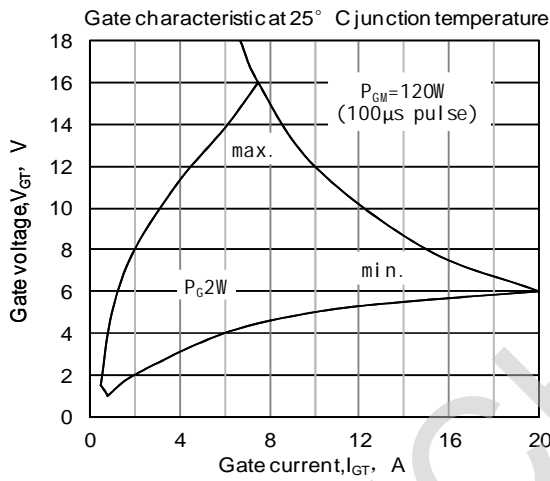


Fig 9

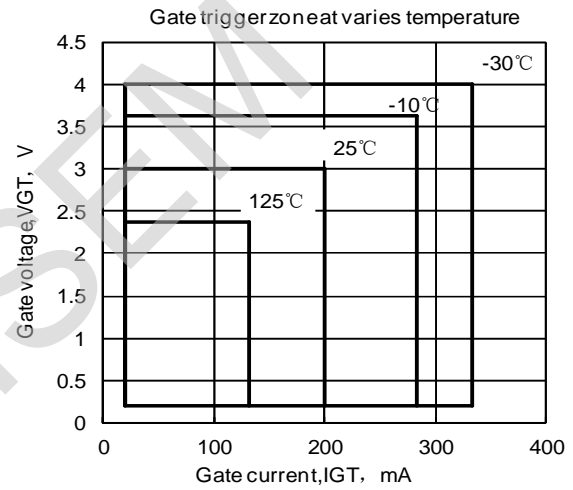
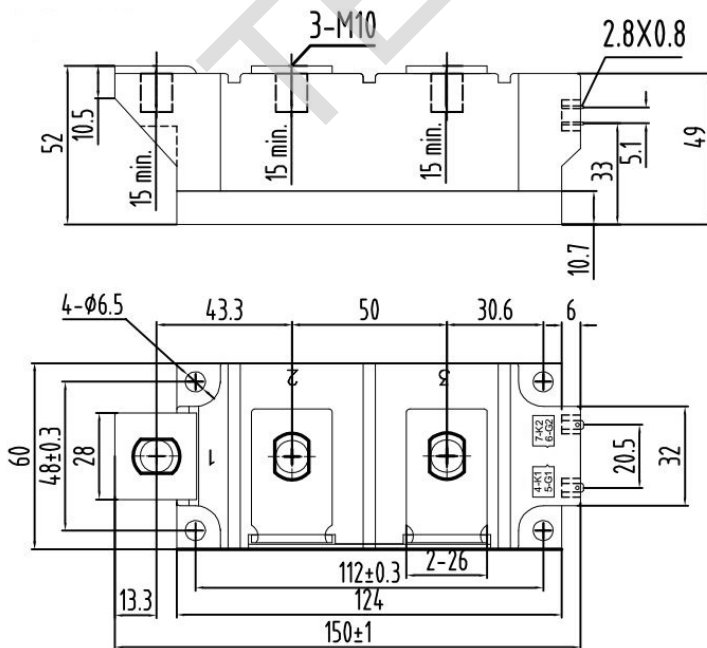
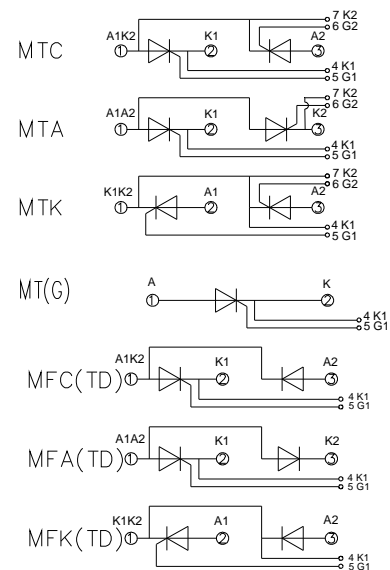


Fig 10

Outline:



Unmarked dimensional tolerance: ±0.5mm



TECHSEM reserves the right to change specifications without notice.