

**Features**

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

Part No. Y24KPA-KT19aT

$I_{T(AV)}$	550A
V_{DRM}, V_{RRM}	200V 400V 600V

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_J(^{\circ}\text{C})$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled	$T_C=70^{\circ}\text{C}$			550	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$t_p=10\text{ms}$	125	200		600	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			16	mA
I_{TSM}	Surge on-state current	10ms half sine wave $V_R=0.6V_{RRM}$	125			8	kA
I^2t	I^2t for fusing coordination					320	$\text{A}^2\text{s} \times 10^3$
V_{TO}	Threshold voltage		125			0.75	V
r_T	On-state slope resistance					0.36	$\text{m}\Omega$
V_{TM}	Peak on-state voltage	$I_{TM}=1000\text{A}, F=5.0\text{kN}$	25			1.41	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			1000	$\text{V}/\mu\text{s}$
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 800A, Gate pulse $t_r \leq 0.5\mu\text{s}$ $I_{GM}=1.5\text{A}$	125			100	$\text{A}/\mu\text{s}$
Q_{rr}	Recovery charge	$I_{TM}=1000\text{A}, t_p=4000\mu\text{s}, di/dt=-20\text{A}/\mu\text{s}, V_R = 100\text{V}$	125		600		μC
I_{GT}	Gate trigger current	$V_A=12\text{V}, I_A=1\text{A}$	25	30		200	mA
V_{GT}	Gate trigger voltage			0.8		2.0	V
I_H	Holding current			20		150	mA
I_L	Latching current					500	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 5.0kN				0.080	$^{\circ}\text{C}/\text{W}$
$R_{th(h-s)}$	Thermal resistance case to heat sink					0.020	
F_m	Mounting force			3.3		5.5	kN
T_{vj}	Junction temperature			-40		125	$^{\circ}\text{C}$
T_{stg}	Stored temperature			-40		140	$^{\circ}\text{C}$
W_t	Weight				60		g
Outline		KT19aT					

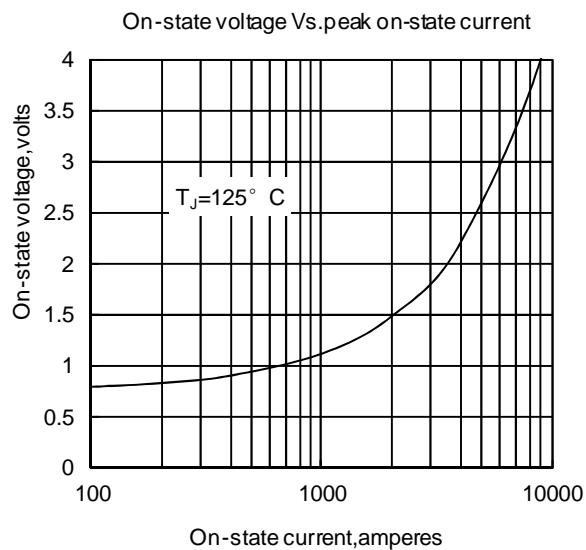


Fig1

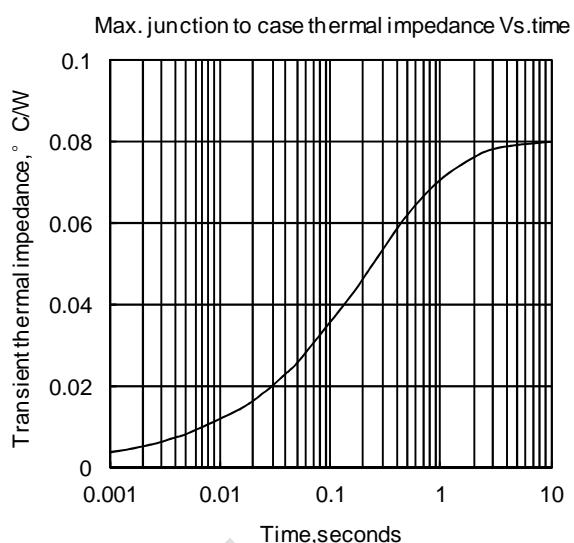


Fig2

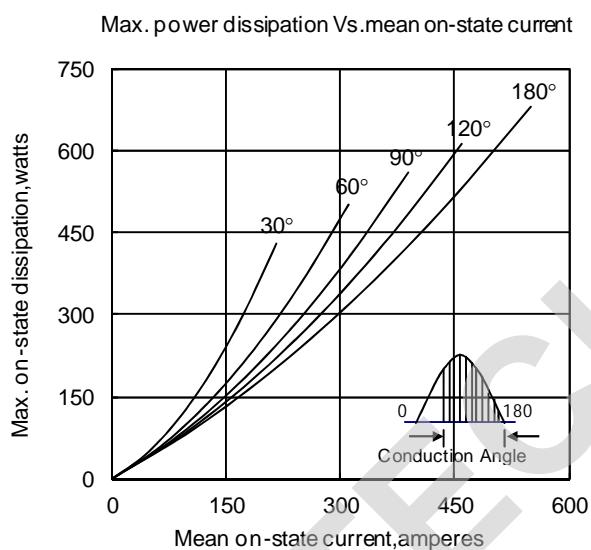


Fig3

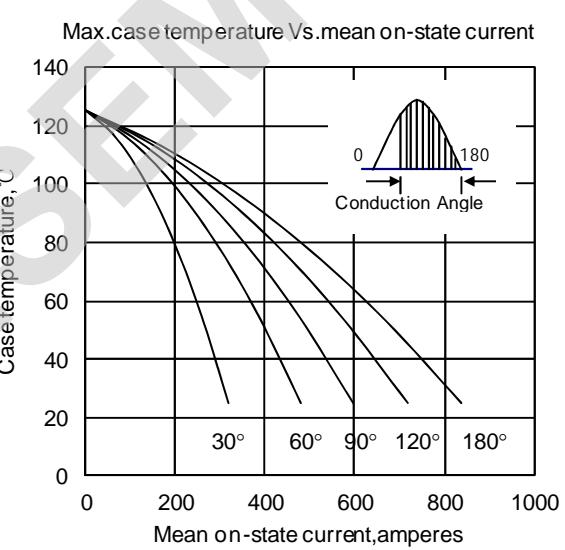


Fig4

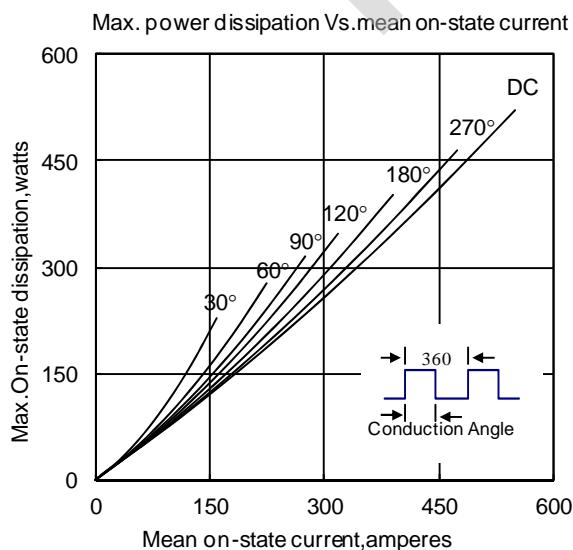


Fig5

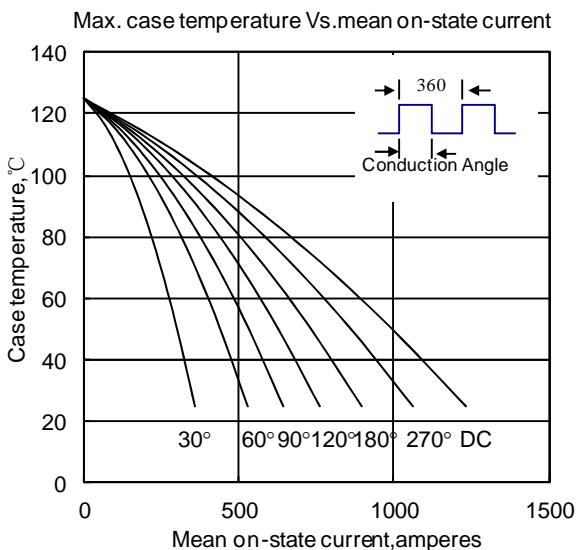


Fig6

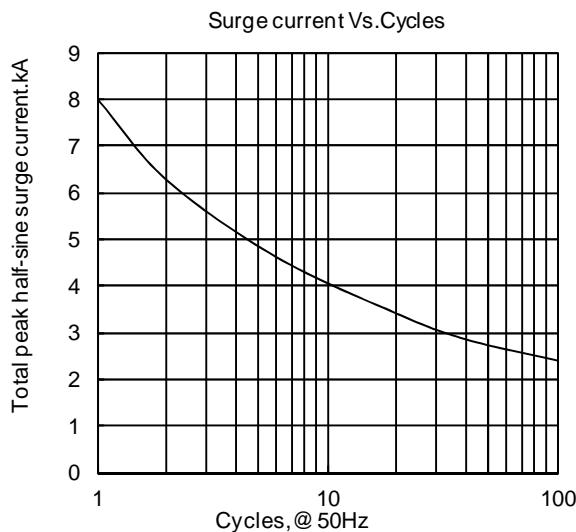


Fig 7

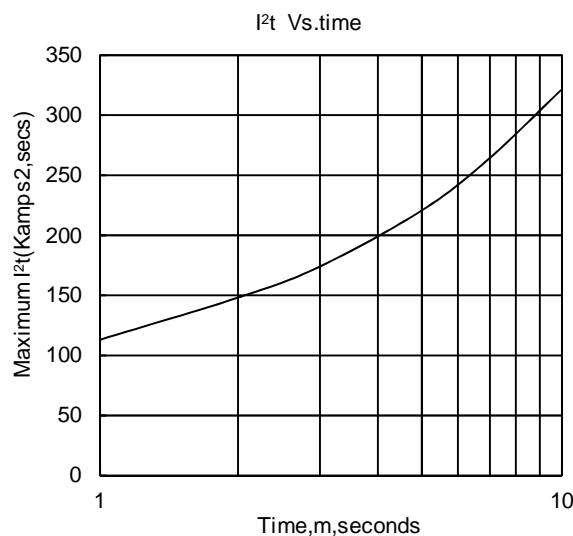


Fig 8

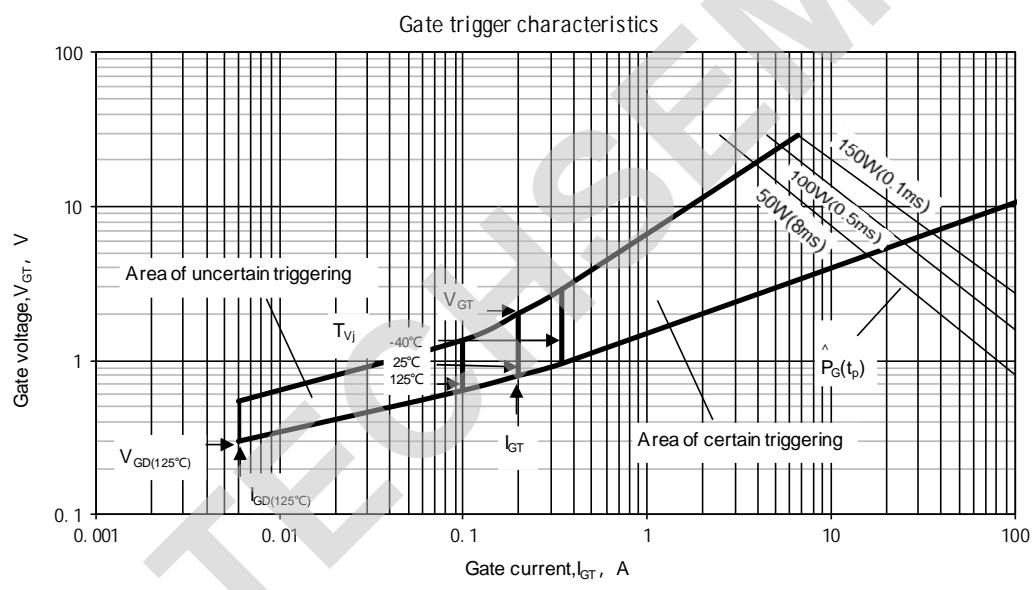
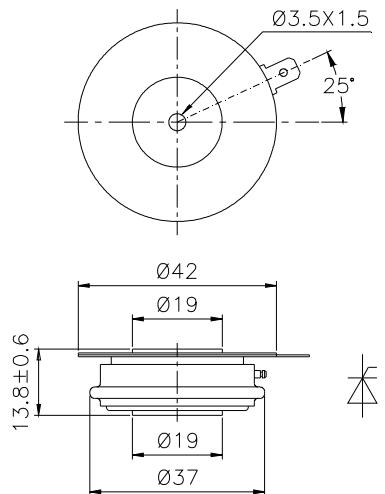


Fig 9

Outline:

TECHSEM reserves the right to change specifications without notice.