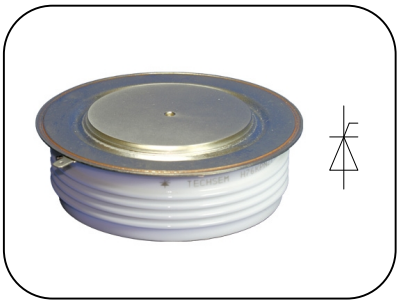


Features：

- **n** Center amplifying gate
- **n** Metal case with ceramic insulator
- **n** Low on-state and switching losses

Typical Applications：

- **n** AC controllers
- **n** DC and AC motor control
- **n** Controlled rectifiers

Part No. H100KPS-KT100dT

$I_{T(AV)}$	**2500A**
V_{DRM},V_{RRM}	**7000V**
	7500V
	8000V

SYMBOL	CHARACTERISTIC	TEST CONDITIONS		T_j(°C)	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled	T_C=70°C	115			2500	A
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} tp=10ms at V_{RRM} tp=10ms		115			600	mA
I_{TSM}	Surge on-state current	10ms half sine wave V_R=0.6V_{RRM}		115			40	kA
I^2t	I^2t for fusing coordination						8000	10^3A^2s
V_{TO}	Threshold voltage			115			1.20	V
r_T	On-state slope resistance						0.31	mΩ
V_{TM}	Peak on-state voltage	I_{TM}=1500A, F=90kN		25			1.60	V
dv/dt	Critical rate of rise of off-state voltage	V_{DM}=0.67V_{DRM}		115			2000	V/μs
di/dt	Critical rate of rise of on-state current	V_{DM}=67%V_{DRM}, Gate pulse t_r≤0.5μs I_{GM}=1.5A		115			200	A/μs
Q_{rr}	Recovery charge	I_{TM}=2000A, tp=4000μs, di/dt=-5A/μs, V_R=100V		115		5000		μC
I_{GT}	Gate trigger current	V_A=12V, I_A=1A		25	40		300	mA
V_{GT}	Gate trigger voltage				0.8		3.0	V
I_H	Holding current				25		200	mA
V_{GD}	Non-trigger gate voltage	V_{DM}=67%V_{DRM}		115			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	Double side cooled Clamping force 90kN					0.0057	°C /W
$R_{th(c-h)}$	Thermal resistance case to heatsink						0.0015	
F_m	Mounting force				81	90	108	kN
T_{vj}	Junction temperature				-40		115	°C
T_{stg}	Stored temperature				-40		140	°C
W_t	Weight					2500		g
Outline	KT100dT							

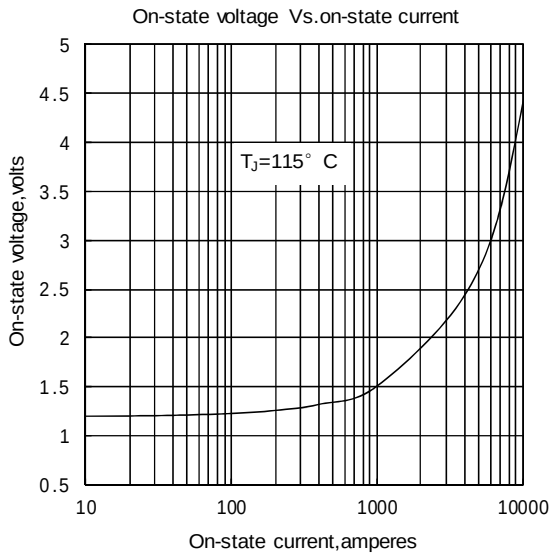


Fig.1

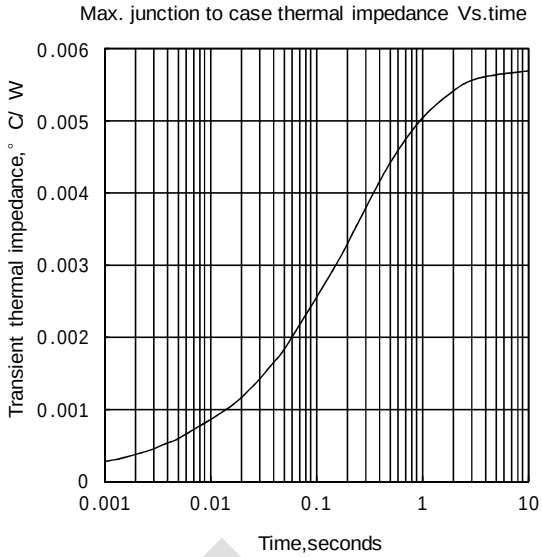


Fig.2

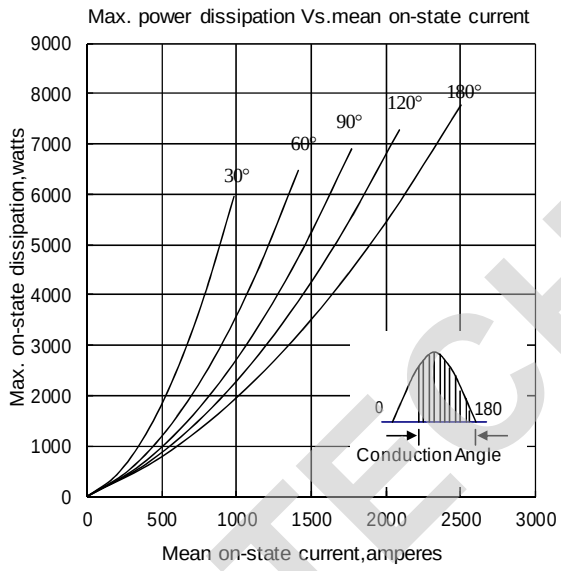


Fig.3

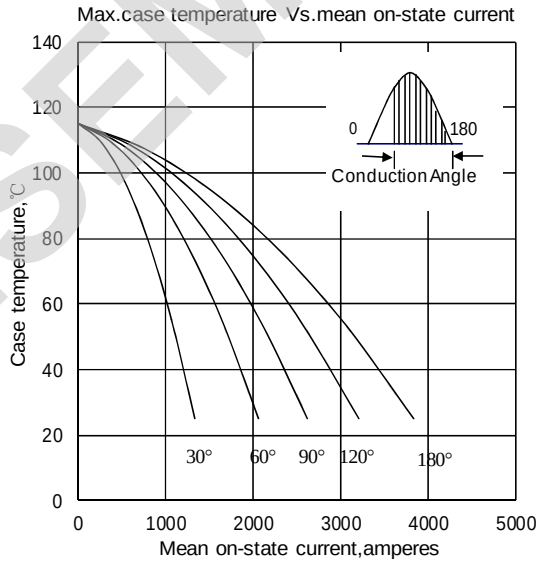


Fig.4

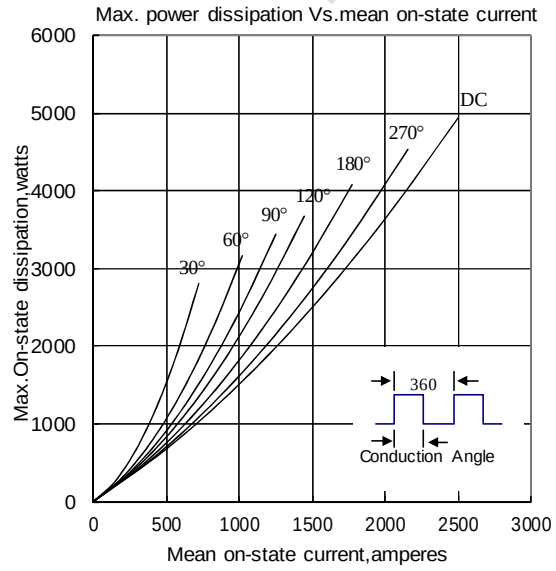


Fig.5

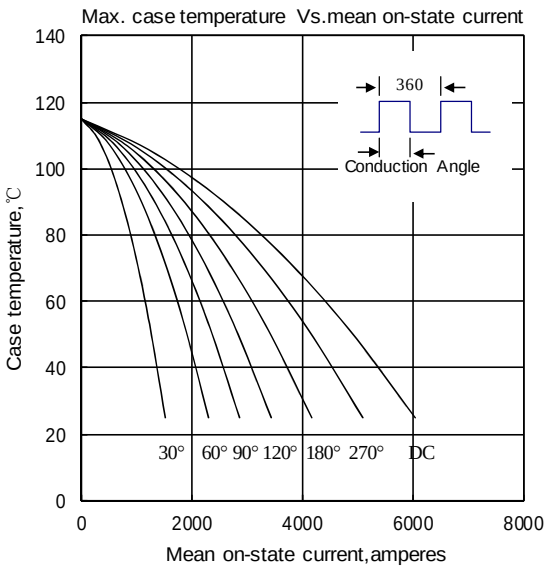


Fig.6

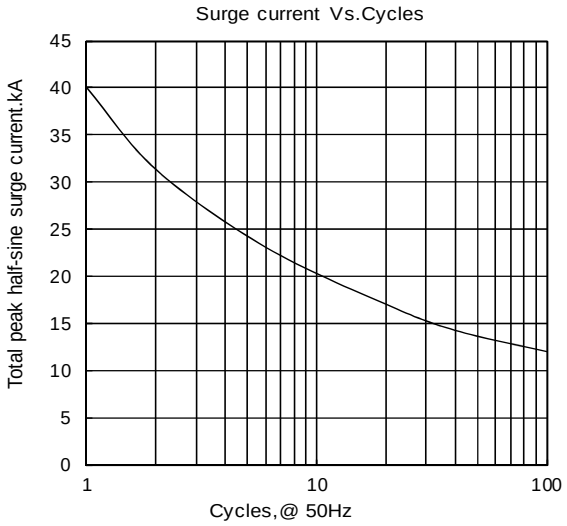


Fig.7

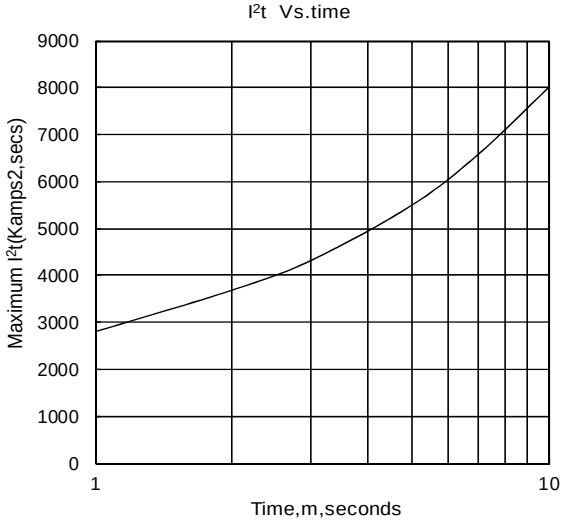


Fig.8

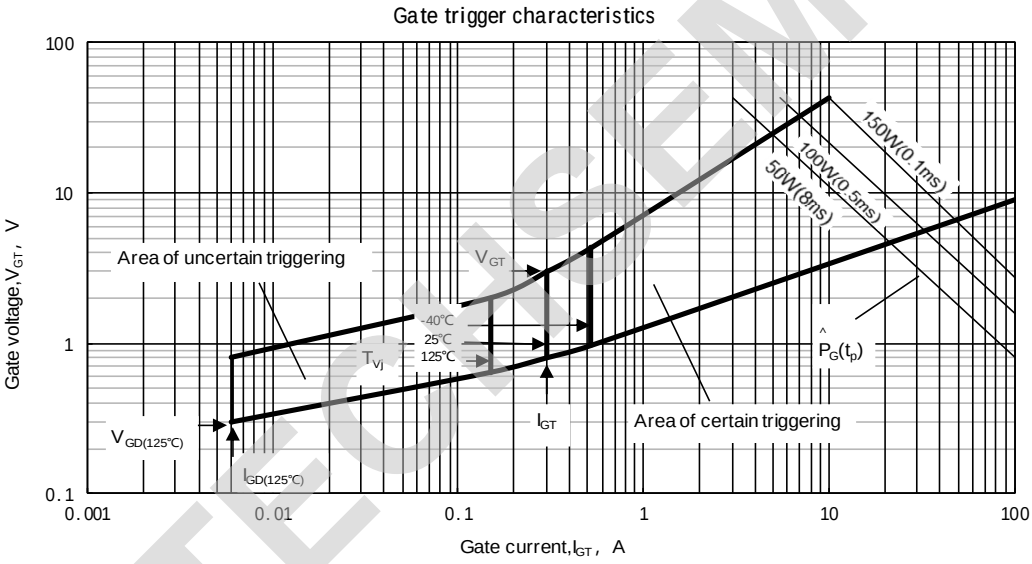
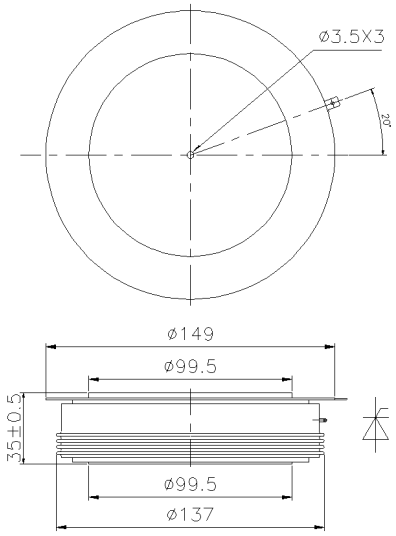


Fig.9

Outline:



TECHSEM reserves the right to change specifications without notice.