**Features:**

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

**Typical Applications**

- AC controllers
- DC and AC motor control
- Controlled rectifiers

**Part No. Y40KPEA-KA37**

$I_{T(AV)}$	1010A
$V_{DRM}, V_{RRM}$	1200V 1400V 1600V 1800V

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j(^{\circ}C)$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	125			1010	A
$V_{DRM}$ $V_{RRM}$	Repetitive peak off-state voltage Repetitive peak reverse voltage	$t_p=10ms$	125	1100		1800	V
$I_{DRM}$ $I_{RRM}$	Repetitive peak current	at $V_{DRM}$ at $V_{RRM}$	125			50	mA
$I_{TSM}$	Surge on-state current	10ms half sine wave $V_R=0.6V_{RRM}$	125			6.4	kA
$I^2t$	$I^2t$ for fusing coordination					205	$A^2s*10^3$
$V_{TO}$	Threshold voltage		125			0.90	V
$r_T$	On-state slope resistance					0.36	mW
$V_{TM}$	Peak on-state voltage	$I_{TM}=1500A, F= 15kN$	25			2.40	V
$dv/dt$	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			300	V/ $\mu$ s
$di/dt$	Critical rate of rise of on-state current	$V_{DM}= 67\%V_{DRM}$ to 1000A, Gate pulse $t_r \leq 0.5 \mu s$ $I_{GM}=1.5A$ Repetitive	125			100	A/ $\mu$ s
$Q_{rr}$	Recovery charge	$I_{TM}=1000A, t_p=4000\mu s, di/dt=-20A/\mu s, V_R=100V$	125		1200		$\mu C$
$I_{GT}$	Gate trigger current	$V_A=12V, I_A=1A$	25	35		250	mA
$V_{GT}$	Gate trigger voltage			0.8		2.5	V
$I_H$	Holding current			20		250	mA
$I_L$	Latching current					500	mA
$V_{GD}$	Non-trigger gate voltage	$V_{DM}= 67\%V_{DRM}$	125			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 15kN				0.030	$^{\circ}C / W$
$R_{th(c-h)}$	Thermal resistance case to heatsink					0.008	
$F_m$	Mounting force			15		20	kN
$T_{vj}$	Junction temperature			-40		125	$^{\circ}C$
$T_{stg}$	Stored temperature			-40		140	$^{\circ}C$
$W_t$	Weight				200		g
Outline		KA37					

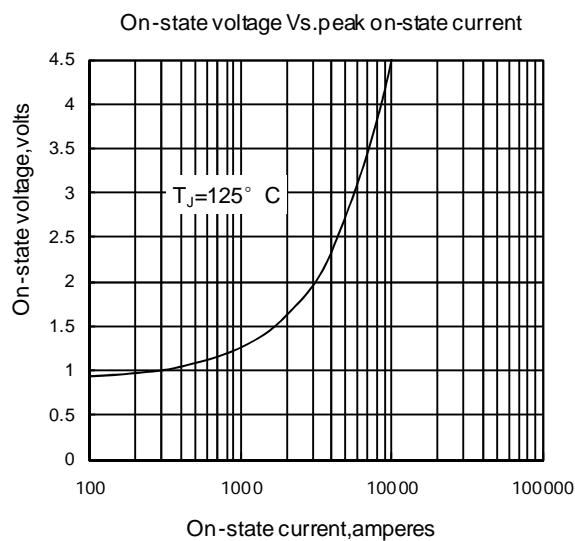


Fig1

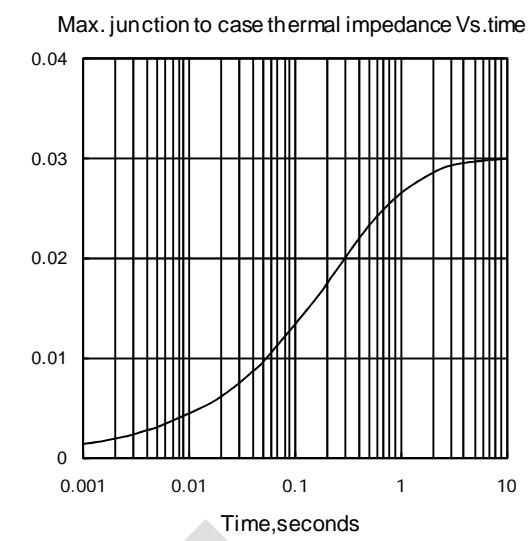


Fig2

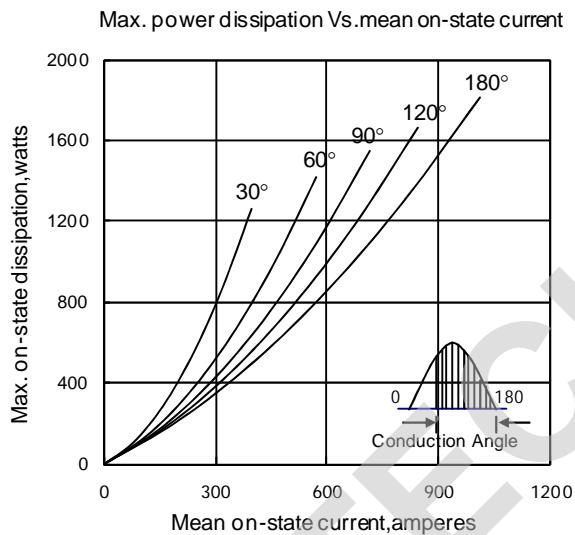


Fig3

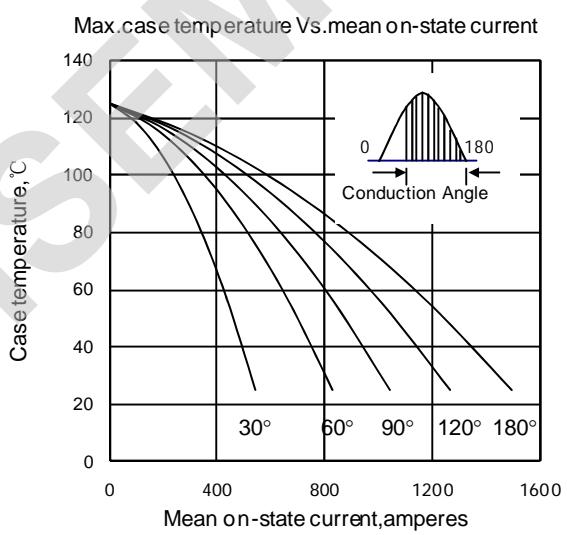


Fig4

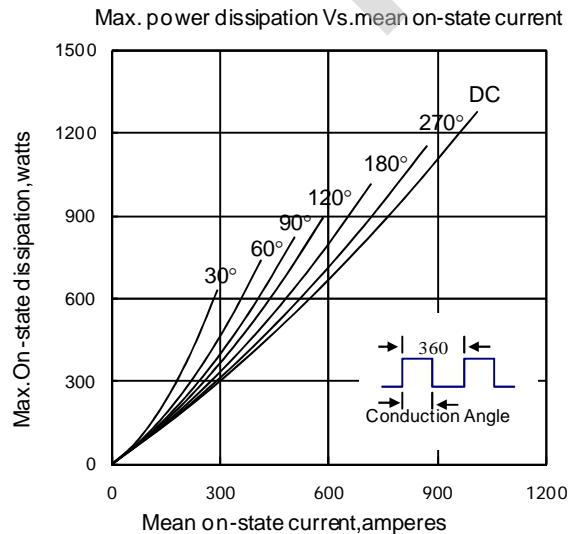


Fig5

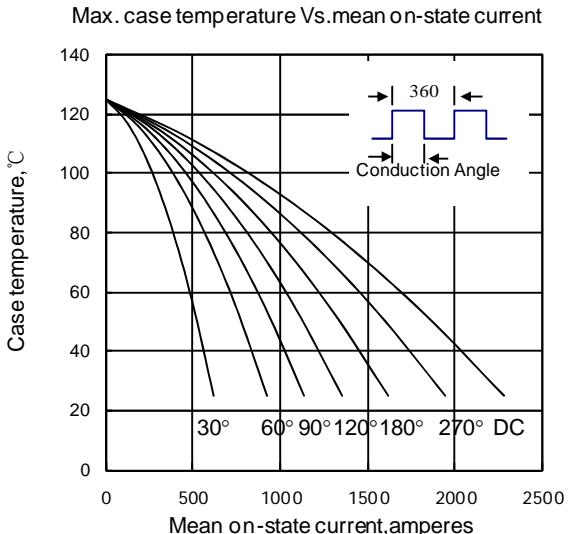


Fig6

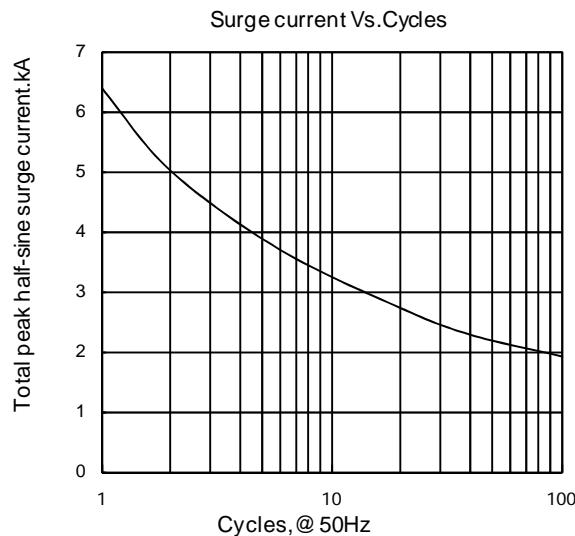


Fig7

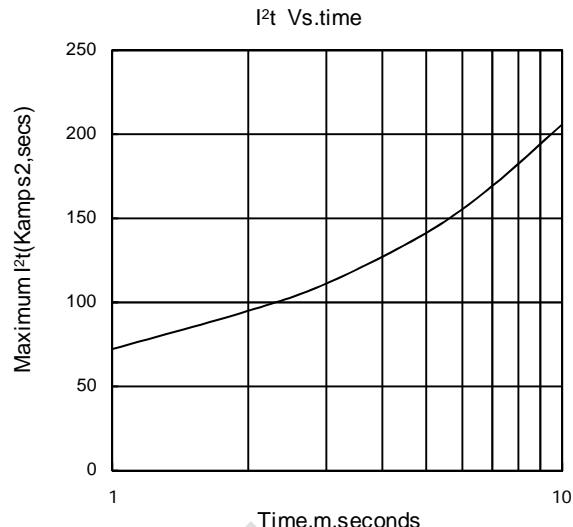


Fig8

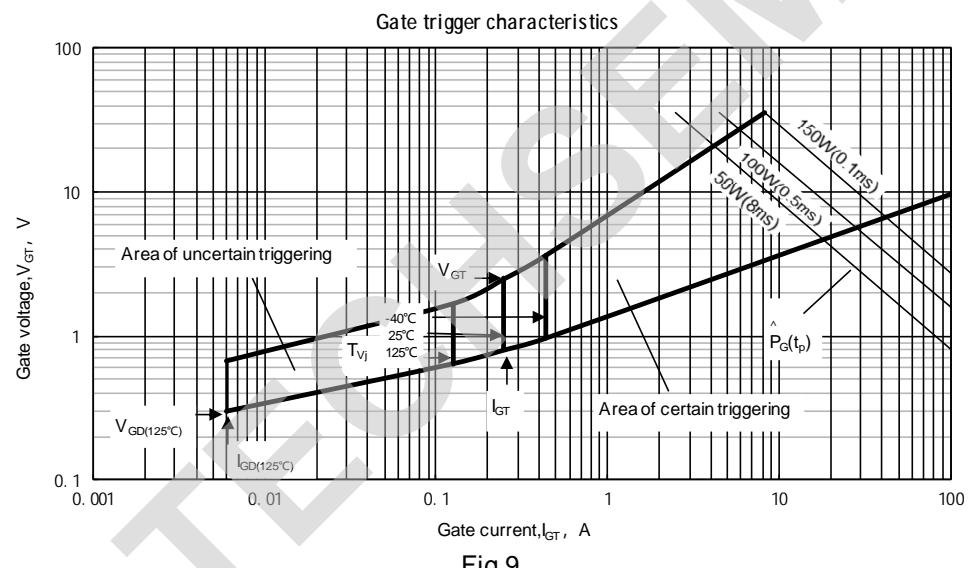
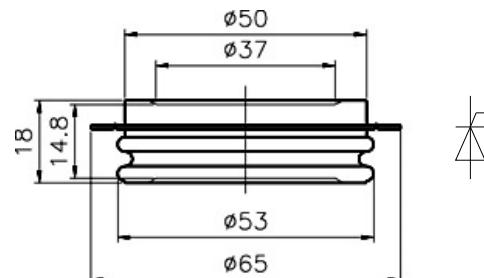


Fig9

**Outline:**

TECHSEM reserves the right to change specifications without notice.