

**Features:**

- Isolated mounting base 3000V~
- Pressure contact technology with Increased power cycling capability
- Space and weight saving

Typical Applications:

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

V_{RRM}, V_{DRM}	Type & Outline
2000V	MFx70-20-223F3
2200V	MFx70-22-223F3
2500V	MFx70-25-223F3

MFx stands for any type of **MFC**, **MFA**, **MFK**

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Single side cooled, $T_c=85^\circ C$	125			70	A
$I_{T(RMS)}$	RMS on-state current					110	A
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			12	mA
I_{TSM}	Surge on-state current			125		1.70	kA
I^2t	I^2t for fusing coordination	$V_R=60\%V_{RRM}$, $t=10ms$ half sine,	125			14.5	$10^3 A^2s$
V_{TO}	Threshold voltage			125		0.92	V
r_T	On-state slope resistance		125			3.20	$m\Omega$
V_{TM}	Peak on-state voltage			25		1.93	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=67\%V_{DRM}$	125			1000	$V/\mu s$
di/dt	Critical rate of rise of on-state current	Gate source 1.5A $t_r \leq 0.5\mu s$ Repetitive	125			200	$A/\mu s$
I_{GT}	Gate trigger current			30		150	mA
V_{GT}	Gate trigger voltage	$V_A=12V$, $I_A=1A$	25	0.7		2.5	V
I_H	Holding current			10		200	mA
I_L	Latching current					1000	mA
V_{GD}	Non-trigger gate voltage			125		0.20	V
$R_{th(j-c)}$	Thermal resistance Junction to case	Single side cooled per chip				0.41	$^\circ C/W$
$R_{th(c-h)}$	Thermal resistance case to heatsink	Single side cooled per chip				0.20	$^\circ C/W$
V_{iso}	Isolation voltage	50Hz, R.M.S, $t=1min$, $I_{iso}:1mA(MAX)$		3000			V
F_m	Terminal connection torque(M5)			2.5		4.0	$N\cdot m$
	Mounting torque(M6)			4.5		6.0	$N\cdot m$
T_{vj}	Junction temperature			-40		125	$^\circ C$
T_{stg}	Stored temperature			-40		125	$^\circ C$
W_t	Weight				175		g
Outline		223F3					

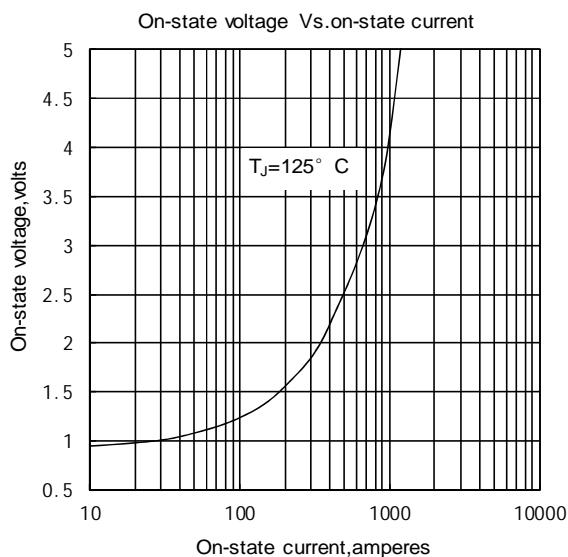


Fig.1

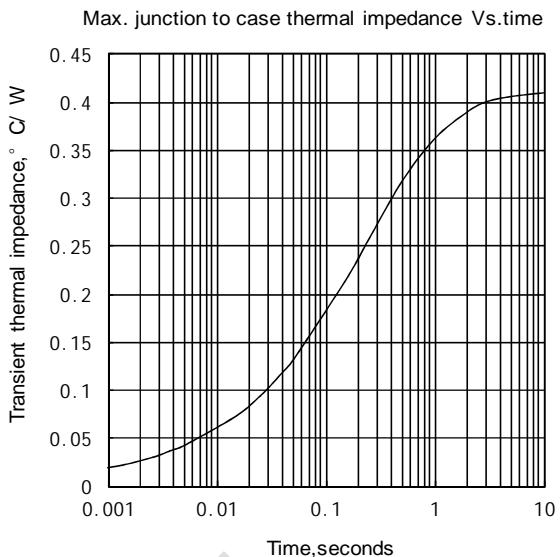


Fig.2

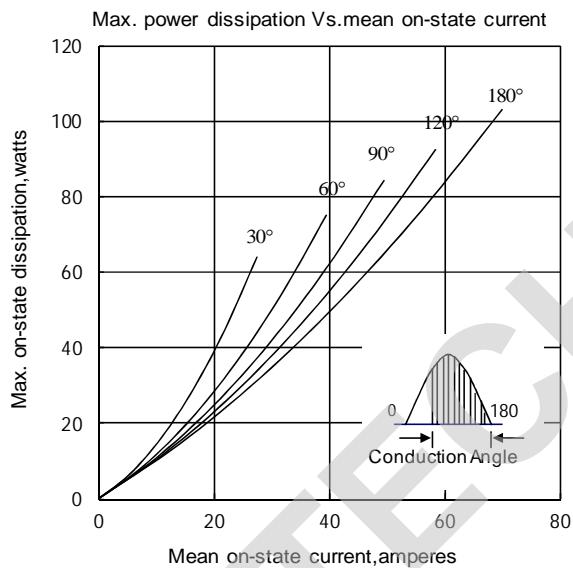


Fig.3

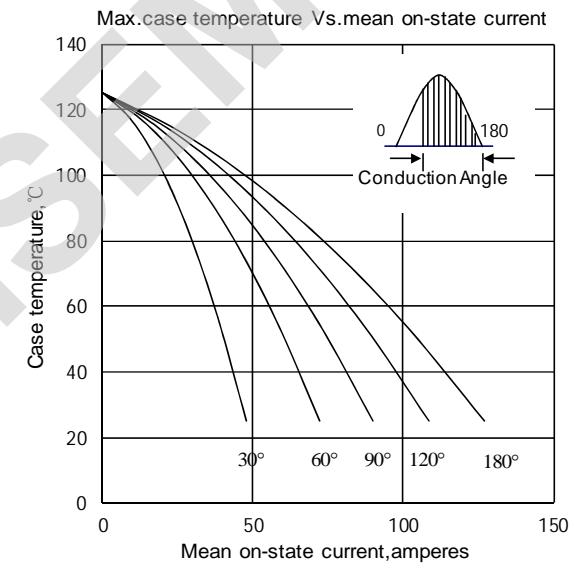


Fig.4

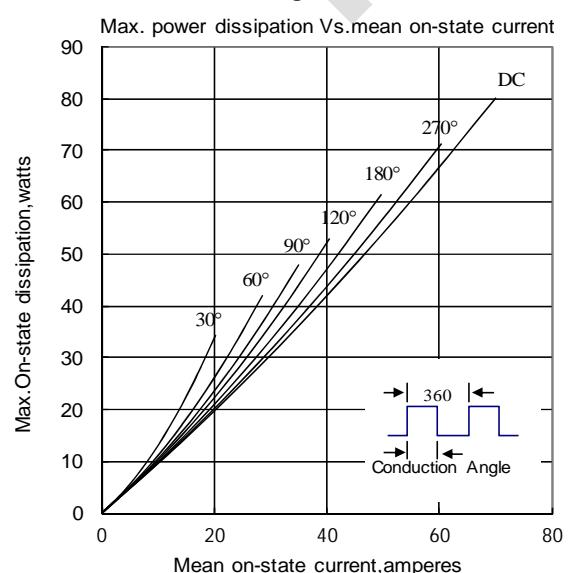


Fig.5

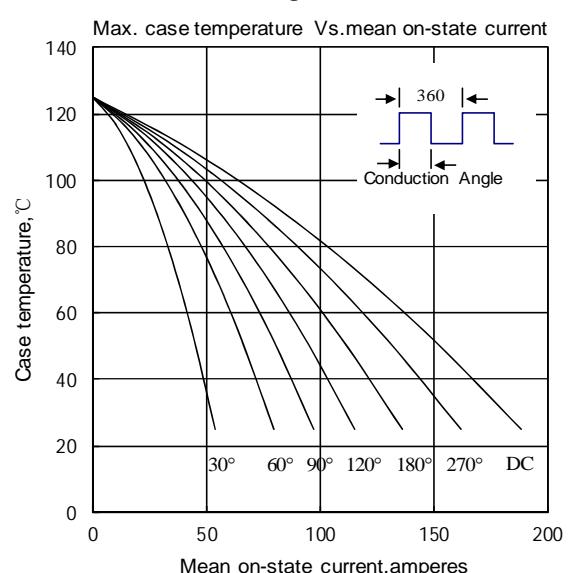


Fig.6

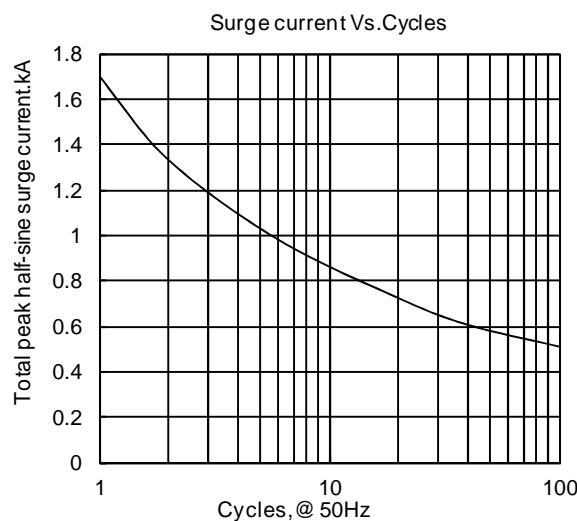


Fig. 7

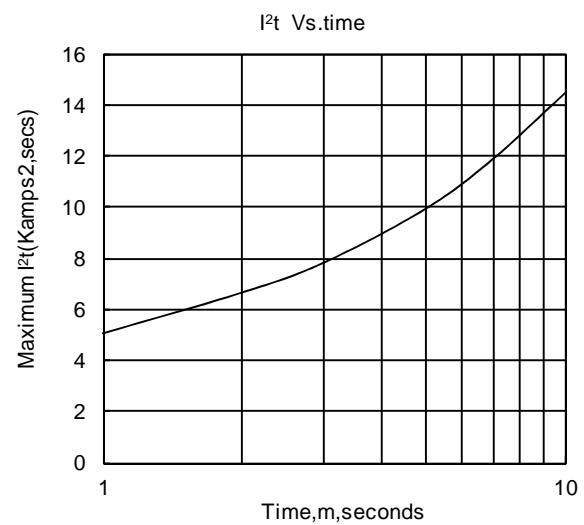


Fig. 8

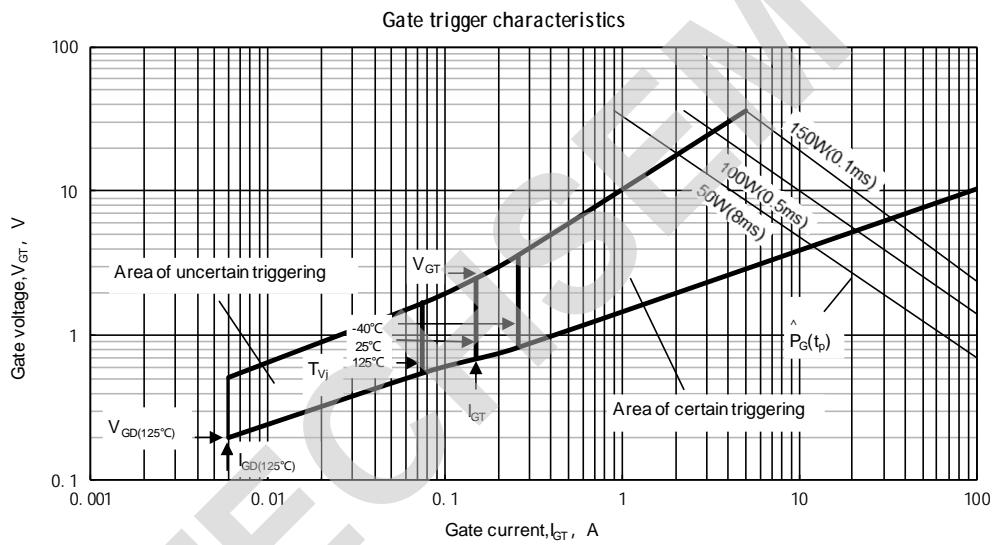
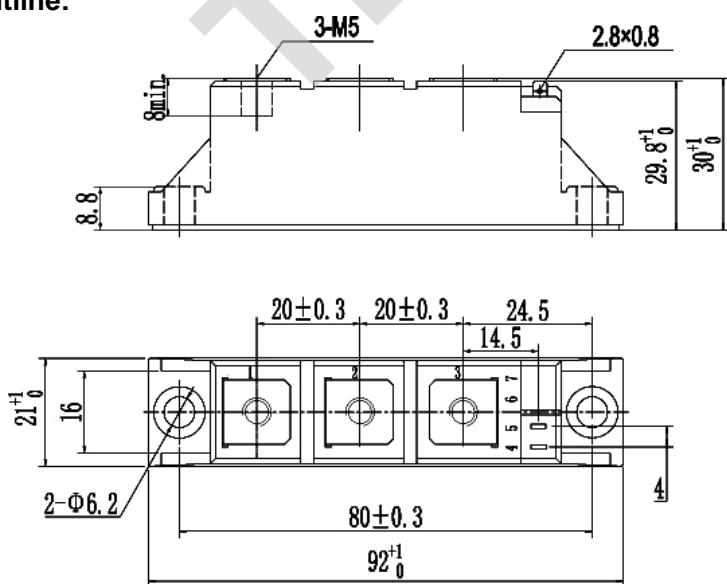


Fig. 9

Outline:**Unmarked dimensional tolerance: ±0.5mm**